Vishay High Power Products

Schottky Rectifier, 2.1 A

FEATURES

- Small foot print, surface mountable
- · Low forward voltage drop
- High frequency operation
- · Guard ring for enhanced ruggedness and long term reliability
- · Compliant to RoHS directive 2002/95/EC
- · Designed and qualified for industrial level

DESCRIPTION

The 10MQ040NPbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _F	DC	2.1	А		
V _{RRM}		40	V		
I _{FSM}	t _p = 5 μs sine	120	А		
V _F	1.5 Apk, T _J = 125 °C	0.56	V		
TJ	Range	- 55 to 150	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	10MQ040NPbF	UNITS	
Maximum DC reverse voltage	V _R	40	V	
Maximum working peak reverse voltage	V _{RWM}	40	v	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 4	I _{F(AV)}	50 % duty cycle at T_L = 123 °C, r On PC board 9 mm ² island (0.013 mm thick copper pad area	0	1.5	A
Maximum peak one cycle non-repetitive surge current		5 μ s sine or 3 μ s rect. pulse	Following any rated load condition and with	120	А
non-repetitive surge current I _{FSM} See fig. 6	10 ms sine or 6 ms rect. pulse	rated V _{RRM} applied	30	A	
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 1 A, L = 6 mH		3.0	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1.0	A



PRODUCT SUMMARY

I_{F(AV)}

 V_{R}





Anode

0

2.1 A

40 V

0

10MQ040NPbF

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop See fig. 1	V _{FM} ⁽¹⁾	1 A	T 05 %C	0.54	V
		1.5 A	Τ _J = 25 °C	0.62	
		1 A	T 105 %C	0.49	
		1.5 A	—— T _J = 125 °C	0.56	
Maximum reverse leakage current See fig. 2	I _{RM} ⁽¹⁾	T _J = 25 °C	V Deted V	0.5	mA
		T _J = 125 °C	V_{R} = Rated V_{R}	26	
Threshold voltage	V _{F(TO)}	T _J = T _J maximum		0.36	V
Forward slope resistance	r _t			104	mΩ
Typical junction capacitance	CT	$V_R = 10 V_{DC}$, $T_J = 25 \ ^\circ C$, test signal = 1 MHz		38	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		2.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T_{J} ⁽¹⁾ , T_{Stg}		- 55 to 150	°C
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation	80	°C/W
Approximate weight			0.07	g
			0.002	oz.
Marking device		Case style SMA (similar D-64)	V1	IF

Note

(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink



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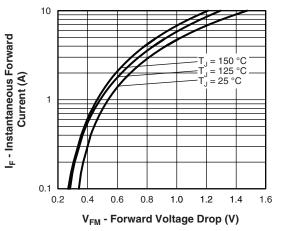


Fig. 1 - Maximum Forward Voltage Drop Characteristics

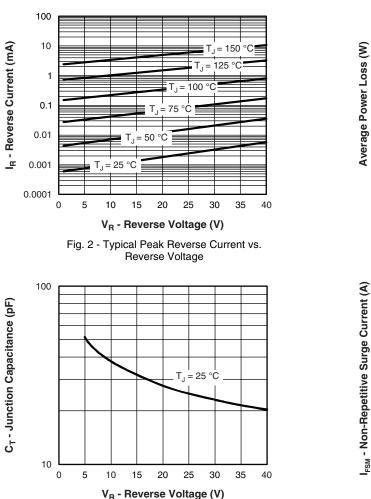
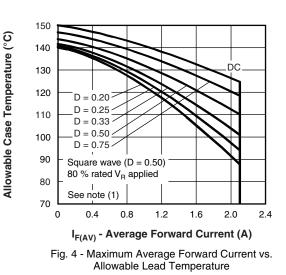


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage



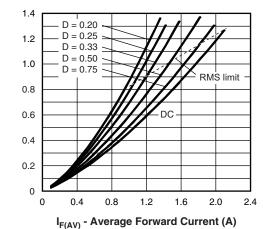
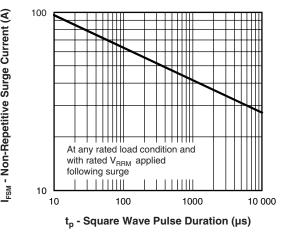
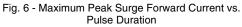


Fig. 5 - Maximum Average Forward Dissipation vs. Average Forward Current





Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

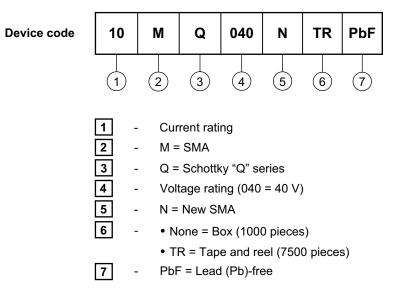
 $Pd = Forward power loss = V_{P(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6); } Pd_{REV} = Inverse power loss = V_{R1} \times I_{R} (1 - D); I_{R} \text{ at } V_{R1} = 80 \% \text{ rated } V_{R}$

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ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95018		
Part marking information	www.vishay.com/doc?95029		
Packaging information	www.vishay.com/doc?95034		
SPICE model	www.vishay.com/doc?95277		



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